

REMARKS

Examiner William Boddie is thanked for the thorough examination and search of the subject Patent Application.

All Claims are believed to be in condition for Allowance, and that is so requested.

Claim 1 of the claimed invention discloses:

1. A system to perform a light show, wherein LED modules are displaying related light beams having defined properties, wherein said properties have been defined prior to performing said light show, is comprising:
  - an **integrated circuit comprising:**
    - an interface to input information;
    - a memory to store the information about the properties of said beams to be displayed;**
    - a sequencer to control one or more LED drivers;
    - a LED driver unit comprising a driver for each color of said LED modules able to control the intensity of light where one driver for each LED is used; and
    - an electrical connection to said LED modules; and
    - an arrangement of one or more LED modules.

Applicant respectfully disagrees that Dowling discloses **an integrated circuit comprising “a memory to store the information about the properties of said beams to be displayed”**, i.e. **an integrated memory**, as the claimed invention does.

Dowling et al disclose in paragraph 53:

"[0053] As used herein, the term **processor may refer** to any system for processing electronic signals. **A processor may include** a microprocessor, microcontroller, programmable digital signal processor or other programmable device, **along with external memory** such as read-only memory, programmable read-only memory, electronically erasable programmable read-only memory, random access memory, dynamic random access memory, double data rate random access memory, Rambus direct random access memory, flash memory, or any other volatile or non-volatile memory for storing program instructions, program data, and program output or other intermediate or final results. **A processor may also, or instead, include an application specific integrated circuit**, a programmable gate array, programmable array logic, a programmable logic device, a digital signal processor, an analog-to-digital converter, a digital-to-analog converter, or any other device that may be configured to process electronic signals

It has to be noted that Dowling et al disclose an **external** memory while the claimed invention discloses an integrated circuit comprising also a memory among all the other components.

In the second sentence of paragraph 53 Dowling discloses "**A processor may include** a microprocessor...(list of devices, which could be referred to the term processor), **along with external memory such as read-only memory**...(list of external memory devices). In the following third sentence Dowling teaches: "**A processor may also, or instead, include an application specific integrated circuit**, a programmable gate array, programmable array logic, a programmable logic device, a digital signal processor, an analog-to-digital converter, a digital-to-analog converter, or any other device that may be configured to process electronic signals.

Applicant believes that the term of the third sentence of Dowling "A processor may also, or instead, include an application specific integrated circuit,...." is referring to the first part of the preceding sentence starting also with "A processor may include a microprocessor...(list of devices, which could be referred to the term processor). Applicant believes that the list of the third sentence refers to additional devices, which can be used with or instead of processor devices listed in second sentence but does not refer to the list of external memories cited at the end of the second sentence.

Furthermore applicant believes that if Dowling had intended to disclose using an integrated memory he would have clearly disclosed it. Dowling does not mention the term "integrated memory" at all in his patent application.

Allowance of all Claims is requested.

It is requested that should the Examiner not find that the Claims are now Allowable that the Examiner call the undersigned at 845-452-5863 to overcome any problems preventing allowance.

Respectfully submitted,



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